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(a)  $n_i = (1.66 \times 10^{15})(100)^{3/2} \exp[-0.66] = (1.66 \times 10^{18}) \exp[-38.37] \exp^{-6} \frac{2(86 \times 10)(100)}{2(86 \times 10)} n_i = 35.9 \text{ cm}^{-3}$   
(b)  $n_i = (1.66 \times 10^{15})(300)^{3/2} \exp[-0.66] = (8.626 \times 10^{18}) \exp[-12.79] \exp^{-6} \frac{2(86 \times 10)(300)}{2(86 \times 10)} n_i = 2.40 \times 10^{13} \text{ cm}^{-3}$   
(c)  $n_i = (1.66 \times 10^{15})(500)^{3/2} \exp[-0.66] = (1.856 \times 10^{19}) \exp[-7.674] \exp^{-6} \frac{2(86 \times 10)(500)}{2(86 \times 10)} n_i = 8.62 \times 10^{15} \text{ cm}^{-3}$  ...

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EX1.1  $\frac{3}{2} \exp \frac{2 g_i E_n}{BT kT} = \frac{2 g_i E_n}{BT kT} = \frac{2(86 \times 10)(100)}{2(86 \times 10)} \ln \frac{2(86 \times 10)(100)}{2(86 \times 10)} = \ln \frac{2(86 \times 10)(100)}{2(86 \times 10)}$   
or  $\frac{3}{2} \ln \frac{2(86 \times 10)(100)}{2(86 \times 10)} = \ln \frac{2(86 \times 10)(100)}{2(86 \times 10)}$   
2.40  $\ln \frac{2(86 \times 10)(100)}{2(86 \times 10)} = \ln \frac{2(86 \times 10)(100)}{2(86 \times 10)}$  EX1.2 (a) (i ...

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A. Neamen Exercise Solutions \_\_\_\_\_ EX1.8  $V_{PS} = I_D R + V_D$  and  $I_D = I_S \exp \left( \frac{V_D}{V_T} \right)$  so  $4 = I_D (4 \times 10^3) + V_D$   $I_D = 4 \times 10^3$  and  $V_D = (10 - 12) \exp \left( \frac{V_D}{0.026} \right)$  By trial and error, we find  $I_D$

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Assume that the BJT in the common-emitter circuit shown in Figure 8.4 has limiting factors of  $\beta$ ,  $V_{CE(sat)}$ , and  $V_{CE(max)}$ . Neglecting second breakdown effects, determine the minimum value of  $R_L$  such that the Q-point of the transistor always stays within the safe operating area for: (a)  $\beta = 100$ , and (b)  $\beta = 200$ . In each case, determine the maximum collector current and maximum transistor power dissipation.

Chapter 8 Solutions | Microelectronics Circuit Analysis ...

## Read Book Microelectronics Neamen Solutions

Donald A. Neamen *Microelectronics: Circuit Analysis and Design* is intended as a core text in electronics for undergraduate electrical and computer engineering students. The fourth edition continues to provide a foundation for analyzing and designing both analog and digital electronic circuits.

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Overview We are Cobham Advanced Electronic Solutions (CAES), a technologically advanced electronics company employing more than 3,600 talented makers, thinkers, innovators, and doers nationwide.

This junior level electronics text provides a foundation for analyzing and designing analog and digital electronics throughout the book. Extensive pedagogical features including numerous design examples, problem solving technique sections, Test Your Understanding questions, and chapter checkpoints lend to this classic text. The author, Don Neamen, has many years experience as an Engineering Educator. His experience shines through each chapter of the book, rich with realistic examples and practical rules of thumb. The Third Edition continues to offer the same hallmark features that made the previous editions such a success. Extensive Pedagogy: A short introduction at the beginning of each chapter links the new chapter to the material presented in previous chapters. The objectives of the chapter are then presented in the Preview section and then are listed in bullet form for easy reference. Test Your Understanding Exercise Problems with provided answers have all been updated. Design Applications are included at the end of chapters. A specific electronic design related to that chapter is presented. The various stages in the design of an electronic thermometer are explained throughout the text. Specific Design Problems and Examples are highlighted throughout as well.

This junior-level electronics text provides a foundation for analyzing and designing analog and digital electronic circuits. Computer analysis and design are recognized as significant factors in electronics throughout the book. The use of computer tools is presented carefully, alongside the important hand analysis and calculations. The author, Don Neamen, has many years experience as an engineering educator and an engineer. His experience shines through each chapter of the book, rich with realistic examples and practical rules of thumb. The book is divided into three parts. Part 1 covers semiconductor devices and basic circuit applications. Part 2 covers more advanced topics in analog electronics, and Part 3 considers digital electronic circuits.

## Read Book Microelectronics Neamen Solutions

This market-leading textbook continues its standard of excellence and innovation built on the solid pedagogical foundation of previous editions. This new edition has been thoroughly updated to reflect changes in technology, and includes new BJT/MOSFET coverage that combines and emphasizes the unity of the basic principles while allowing for separate treatment of the two device types where needed. Amply illustrated by a wealth of examples and complemented by an expanded number of well-designed end-of-chapter problems and practice exercises, Microelectronic Circuits is the most current resource available for teaching tomorrow's engineers how to analyze and design electronic circuits.

Introduction to Circuit Analysis and Design takes the view that circuits have inputs and outputs, and that relations between inputs and outputs and the terminal characteristics of circuits at input and output ports are all-important in analysis and design. Two-port models, input resistance, output impedance, gain, loading effects, and frequency response are treated in more depth than is traditional. Due attention to these topics is essential preparation for design, provides useful preparation for subsequent courses in electronic devices and circuits, and eases the transition from circuits to systems.

Microelectronic Circuit Design is known for being a technically excellent text. The new edition has been revised to make the material more motivating and accessible to students while retaining a student-friendly approach. Jaeger has added more pedagogy and an emphasis on design through the use of design examples and design notes. Some pedagogical elements include chapter opening vignettes, chapter objectives, "Electronics in Action" boxes, a problem solving methodology, and "design note" boxes. The number of examples, including new design examples, has been increased, giving students more opportunity to see problems worked out. Additionally, some of the less fundamental mathematical material has been moved to the ARIS website. In addition this edition comes with a Homework Management System called ARIS, which includes 450 static problems.